

# M.Tech

**I - Year**

**I – SEM**

## Computer Science

Sl.No	SUBJECT	L	P	C	INT	EXT	TOTAL
MCS1.1	Data structures and algorithm analysis	4	-	8	40	80	100
MCS102	Mathematical Foundations of Computer Science	4	-	8	40	80	100
MCS1.3	Computer Organization and Architecture	4	-	8	40	80	100
MCS1.4	Database Management Systems	4	-	8	40	80	100
MCS1.5	Operating Systems	4	-	8	40	80	100
MCS1.6	Object Oriented Programming	4	-	8	40	80	100
MCS1.7	Data structures Lab		4	4	40	80	100
MCS1.8	Systems Lab-1 (Covering the experiments Operating systems, Database Management systems)		4	4	40	80	100

**I - Year**

**II – SEM**

Sl.No	SUBJECT	L	P	C	INT	EXT	TOTAL
MCSE2.1	Data warehousing and data mining	4	-	8	40	80	100
MCSE2.2	Computer Networks	4	-	8	40	80	100
MCSE2.3	Object oriented software Engineering	4	-	8	40	80	100
MCSE2.4	Web Technologies	4	-	8	40	80	100
MCSE2.5	Elective 1	4	-	8	40	80	100
	MCSE2.5.1 Mobile Computing MCSE2.5.2 Bio-Informatics MCSE 2.5.3 Compiler Design MICSE 2.5.4 Human Computer Interaction				40	80	100
MCSE2.6	Elective 2	4	-	8	40	80	100
	MCSE 2.6.1 Artificial Intelligence and soft computing MCSE 2.6.2 Secured Database applications development MCSE 2.6.3 Middleware and enterprise integration technologies MCSE 2.6.4 Image processing and pattern recognition				40	80	100
MCSE 2.7	Application Development Lab (covering the experiments : Mining tools, UML, Rational Tools)		4	4			
MCSE 2.8	Web Technologies Lab		4	4			

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## Embedded System

Code	Name of the Subject	L	P	C	INT	EXT	TOTAL
<b>Core</b>							
	1. Embedded Systems Concepts	4	-	8	40	60	100
	2. Embedded System Design	4	-	8	40	60	100
	3. VLSI Technology & Design	4	-	8	40	60	100
	4. Analog & Digital IC Design	4	-	8	40	60	100
<b>Elective I</b>							
	1. Embedded Software Design	4	-	8	40	60	100
	2. VHDL Modeling of Digital Systems						
<b>Elective II</b>							
	1. Hardware Software Co-Design	4	-	8	40	60	100
	2. Embedded & Real Time Systems						
<b>Laboratory</b>							
	1.HDL Programming Laboratory	-	4	4	40	60	100

I - Year

II – SEM

Code	Name of the Subject	L	P	C	INT	EXT	TOTAL
<b>Core</b>							
	1. Algorithms for VLSI Design Automation	4	-	8	40	60	100
	2. Real Time operating Systems for Embedded Systems	4	-	8	40	60	100
	3. DSP Processors & Architecture	4	-	8	40	60	100
	4. Advanced Microcontrollers and Processors	4	-	8	40	60	100
<b>Elective III</b>							
	1. System Modeling & Simulation	4	-	8	40	60	100
	2. Design of Fault Tolerant Systems						
<b>Elective IV</b>							
	1. Low power VLSI Design	4	-	8	40	60	100
	2. CPLD and FPGA Architecture and Applications						
<b>Laboratory</b>							
	1.Embedded Systems Laboratory	-	4	4	40	60	100